



**Shri Sant Gajanan Maharaj College of
Engineering**
SHEGAON, MS, INDIA
Newsletter December 2015

**DEPARTMENT OF ELECTRONICS AND
TELECOMMUNICATION ENGINEERING
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**World Class
Engineering and
Management in
The
Environment of
Spiritual
Foundation to
Serve the Global
Society**

ABOUT DEPARTMENT:

The department has taken development as a continuous process and assured this through its mission. The department has blend of senior, young and dynamic staff. The department has following laboratories equipped with modern instruments and advanced software.

- Digital Signal Processing Lab. / VLSI-Embedded System Design. Lab.
- Power Electronics (Instrumentation) Lab. / Project Lab.
- M.E. Computer Lab
- Communication Engineering (UHF) Lab.
- Analog and Digital Circuit Lab./ Electronics Devices and Circuit Lab.
- Electronics Work Shop Lab

To cope up with the advanced curriculum of the university, all these laboratories are equipped with special instruments like Wobuloscope, Emulator, OTDR simulation package, Xilinx 6.4, PLDs, TMS 380C50 DSP trainer kits, Klystron & Gunn benefits.

Dr. G.S. Gawande Head, Department of Electronics & Tele. Engineering

VISION:

- To impart quality education and excel in Electronics and Telecommunication Engineering research to serve the global society.

MISION:

- To develop excellent learning center through continuous interaction with Industries, R&D centers and Academia.
- To produce competent, entrepreneurial and committed Electronics and Telecommunication Engineers.
- To develop state-of-the -art infrastructure, centers of excellence and to pursue research of global and local relevance.
- To inculcate ethical, spiritual and human values to serve the global society.

Program Educational Objectives (PEOs)

- **PEO1:** To produce Electronics & Telecommunication engineers with a strong foundation of Mathematics, Science and Technology to fulfill needs of society.
- **PEO2:** To enable students to innovate design, simulate, develop, analyze and test hardware and software components for offering solutions to real life situations using state-of-the-art infrastructure and R&D facilities.
- **PEO3:** To nurture students with professional attitude, leadership, entrepreneurship, effective communication, teamwork & multi-disciplinary approach to serve in national and multinational organizations.
- **PEO4:** To inculcate ethical, moral and environment friendly values in students.

Program Outcomes (POs)

At the end of the program, a graduate will –

- [1] Develop an ability to apply knowledge of mathematics, science and engineering in appropriate fields of Electronics & Tele-communication engineering practice. (Engg. Knowledge)
- [2] Inculcate skills to design and conduct experiments, as well as to analyze and interpret data. (Problem Analysis)
- [3] Develop an ability to disseminate the knowledge of Components & Circuit Design, Communication Systems, Micro-computing & Digital System Design & implement them with latest technology. (Design ability)
- [4] Develop an ability to use research based knowledge using relevant literature survey to design experiments, mini projects, projects to analyze, interpret and synthesis the results to get valid conclusions. (Solutions to complex problem)
- [5] Develop an ability to use modern engineering & IT tools and techniques to solve industrial and engineering problems. (Use of modern tools)
- [6] Show the understanding of impact of engineering solutions on the society and also will be aware of contemporary issues. (The Engineer and the society)
- [7] Develop an understating of professional, moral, cultural and environmental responsibilities while imparting Electronics & Telecommunication Engineering solutions. (Environment and sustainability)
- [8] Inculcate an ability to apply principles of professional ethics and responsibilities while implementing engineering practices. (Professional responsibilities & Ethics)
- [9] Demonstrate an ability to work efficiently in the capacity of an individual, member or leader of a diverse team including multi-disciplinary tasks. (Team work)
- [10] Communicate effectively in both verbal and written form not only with engineers but also with the society at large. (Communication skills)
- [11] Be able to apply principles of project management and finance while effectively completing engineering and multi-disciplinary projects as an individual, a member or a leader. (Project management and finance)
- [12] Constantly perceive the need of consistent professional up gradation and will pursue independently the technological developments through trainings and higher studies. (Life long learning)

Program Specific Outcomes (PSOs)

- **PSO1** Students will be able to apply the fundamental and design knowledge in the areas of analog and digital circuits and systems for solving the real world engineering problems
- **PSO2** Students will be able to apply the fundamental knowledge for the analysis and development of communication based circuits and systems.

Recent publication by faculty:

Recent journal publication:

Sr. No.	Author	Title of the paper	Name of the journal	Volume No.	Month and year of publication	Impact factor
1	Dr. R. S. Dhekekar	A review on real time implementation of FPGA based system	SSRG-IJECE	Volume 2 issue 3	June 2015	2.03
2	Prof . D.D. Nawgaje	Electronic toll collection system based on ARM	International journal of science engineering and technology research (IJSETR)	Volume 4, Issue 1,	June 2015	5.762

Recent Conference Publication:

Sr. No.	Author	Title of the paper/book published	Name of the journal/Book	Month and year of publication
1.	Prof .V. M. Umale	Digital image watermarking using QR code	International conference on emerging trends in engineering & technology (ICETIET) at institute for research and academic journal's Mumbai.	August 2015
2.	Prof . M.N.Tibdewal	Detection and classification of eye blink artifact in electroencephalogram through discrete wavelet transform and neural N/W	IEEE-Pune Section's International conference on pervasive computing	Jan. 2015
3.	Prof.G.S.Gawande & Dr. K.B. Khanchandani	Performance enhancement of multirate digital filter structures using advanced DSP optimization techniques	IEEE International conference on information processing	16-19 Dec 2015

4.	Prof.G.S.Gawande & Dr. K.B. Khanchandani	Efficient design and FPGA Implementation of digital filter for audio application (ICCUBEA 2015)	IEEE International conference on computing, communication, control and automation	26-27 Feb.2015
5.	Prof. Faizan I.Khandwani	Techniques to slice password for preserving the privacy in cloud computing	3 rd International conference on emerging trends & research in engineering & technology. In association with international journal of Pure & applied research in engineering & technology	April 2015
6.	Prof .S. P. Badar	High speed fit processor using rodirx 2 pipeline architecture	IEEE ICIC at COEP, Pune	May 2015
7.	Prof . P. R. Wankhede	Blood vessel segmentation using graph cut analysis	IEEE ICIC at COEP, Pune	May 2015

Guest lecturers arranged in the department:

- Guest lecture on “FPGA architecture and applications” was organized by the department of ExTC on 27th July 2015 for all the BE final year students, the lecture was delivered by Dr. Kishor Sarawdekar, Associate Prof. IIT (BHU) Varanasi.
- Guest lecture on “Microwave communication” was organized by the Department of ExTC on 5th August 2015 for all the BE final year students, The lecture was delivered by Dr. V. M. Pandharipande, Ex.Vice-Chancellor, Marathwada University, Aurangabad

Workshop arranged in the department:

Sr. No.	Title of activity	Organizer	Dates
1.	Simulation of microwave circuits and antennas using keysight's advanced design system (ADS)”	Prof. V. V. Ratnaparkhi	25/07/2015 to 26/07/2015
2.	Research & Development orientation program	Dr. K. B. Khanchandani Dr. R. S. Dhekekar	15/09/2015
3.	RF Circuits and Components : Design, Analysis and Measurements	Prof. V. V. Ratnaparkhi	03/10/2015 to 04/10/2015
4.	Workshop on Adriano using MATLAB	Prof .P. R. Wankhede	28/12/2015 to 30/12/2015

Final year placement statistics:

Sl.No.	Name of student placed	Name of the employer	Appointment letter reference no. with Date
1	Aadesh Tonge	TCS Ltd., Pune	30/09/15
2	Trupti Fating	TCS Ltd., Pune	30/09/15
3	Sankalp Gujar	TCS Ltd., Pune	30/09/15
4	Sakshi Rikhe	TCS Ltd., Pune	30/09/15
5	Nita Kaple	TCS Ltd., Pune	30/09/15
6	Shraddha Panpaliaya	TCS Ltd., Pune	30/09/15
7	Mahi Agrawal	TCS Ltd., Pune	30/09/15
8	Durgesh Narute	TCS Ltd., Pune	30/09/15
9	Aditya Shastri	TCS Ltd., Pune	30/09/15
10	Satish Tayade	TCS Ltd., Pune	30/09/15
11	Mayank Aserkar	TCS Ltd., Pune	30/09/15
12	Ghanshyam Tawari	TCS Ltd., Pune	30/09/15
13	Mohini Karwa	TCS Ltd., Pune	30/09/15
14	Rasika Pande	TCS Ltd., Pune	30/09/15
15	Payal Surkar	TCS Ltd., Pune	30/09/15
16	Shubham Belsare	TCS Ltd., Pune	30/09/15
17	Goraknath Pawar	TCS Ltd., Pune	30/09/15
18	Cheral Bhati	TCS Ltd., Pune	30/09/15
19	Sumedh Wakode	TCS Ltd., Pune	30/09/15
20	Ritu Hingmire	TCS Ltd., Pune	30/09/15
21	Krutika Malpani	TCS Ltd., Pune	30/09/15
22	Payal Wankhade	TCS Ltd., Pune	30/09/15
23	Akshay Bante	TCS Ltd., Pune	30/09/15
24	Ruchika Shah	TCS Ltd., Pune	30/09/15
25	Mangesh Shinde	TCS Ltd., Pune	30/09/15
26	Ganesh Dighole	TCS Ltd., Pune	30/09/15
27	Izram Shaikh	TCS Ltd., Pune	30/09/15
28	Akshay Suradkar	TCS Ltd., Pune	30/09/15
29	Ashish Tabade	TCS Ltd., Pune	30/09/15
30	Prajakta Jawanjal	TCS Ltd., Pune	30/09/15
31	Shweta Rakhe	TCS Ltd., Pune	30/09/15
32	Aishwarya Gandhi	TCS Ltd., Pune	30/09/15
33	Kundan Pandey	TCS Ltd., Pune	30/09/15
34	Nikita Sikachi	TCS Ltd., Pune	30/09/15

Faculty visited to industry

Name of faculty	Name of industries	Outcome of industry visit
Prof. V.M.Umale Prof. D.L.Bhombe	Tata Motors, Pune	Industrial visit, internship, sponsored projects
Prof. M. N.Tibdewal Prof. K.T.Kahar	Renu Electronics, Pune	Industrial visit, internship, expert lecture
Prof. G.S.Gawande Prof. A.N.Dolas	Electronica, Pune	Industrial visit, internship

Prof. V.N.Bhonge Prof. S.P.Badar	Mitsubishi Electric, Pune	Industrial visit, training, internship, sponsored projects
Prof. D.D.Nawgaje Prof. P.R.Wankhede	General Motors	Internship, sponsored projects
Prof. V.K.Bhangdiya Prof. V.V.Ratnaparkhi	Indian Technologies, Pune	Internship, expert lecture, sponsored projects
Prof. Dr.R.S.Dhekekar	Soyo Systems, Jalgaon	Internship, sponsored projects

faculty as participants in faculty development/training activities :

Name of the faculty	Name of program	Place	Date
Dr. J.S. Bakare	CEP course coordinators workshop on “Engineering Physics”	IIT Bombay	7-11 Sep. 2015
Prof. V. M. Umale	One day workshop on ‘Scheme updation of the programs in board of studies (Electronics Engg.)	SSGMCE, Shegaon	30 Dec. 2015
Prof. V. N. Bhonge	One day workshop on ‘Scheme updation of the programs in board of studies (Electronics Engg.)	SSGMCE, Shegaon	30 Dec. 2015
Prof. V. V. Ratnaparkhi	QIP Short Term Course on “Antennas and Microwave Passive Components-Design and Measurements”	IIT, Kharagpur	22-28 June 2015
Prof. K. T. Kahar	One day workshop on ‘Scheme updation of the programs in board of studies (Electronics Engg.)	SSGMCE, Shegaon	30 Dec. 2015
Prof. Swapnil P. Badar	One day workshop on ‘Scheme updation of the programs in board of studies (Electronics Engg.)	SSGMCE, Shegaon	30 Dec. 2015
	Three days workshop on “Arduino using MATLAB”	SSGMCE, Shegaon	28-30 Dec. 2015

Student's achievements :

Sr.No	Name	Date	Event	Conducted by	Event outcome
1	Aboli Bajare	26/12/15 to 28/12/15	TECHFEST	IIT Bombay	Technical paper writing and presentation
2	Linika Darokar				
3	Gauri Gawande				
4	Pratisha Padishalwar				
5	Vinita Agrawal	22/12/2015 to 23/12/2015	Project Competition	MIT Pune	Public speaking skill
6	A.M.Shashtri				
7	D.A.Narute				
8	G.G.Pawar				
9	M.U. Vyavhare				
10	Aabha Raut				
11	Sima Patil				

Sponsored projects:

Project group No.	Class	Name of Students	Guide
PRG 31	4U1	Shubham Vivek Manekar	Prof. V.K. Bhangdiya
Smart LED dimmer (Sponsored by Indieon Tech Pvt. Ltd, Pune)		Pranav Ramanlal Pardhi	
		Rohan Digambar Sherekar	
		Umesh Gajananrao Charpe	
PRG 35	4U1	Mithilesh Nilkanth Naphade	Prof. P.D.Kale
Multi-functional antenna with switchable polarization (Sponsored by National Instruments, Pune)		Saurabh Rajkumar Deshmukh	
		Ku. Payal Upendrarao Wankhade	
		Ku. Dipika Gajanan Deotalu	

Workshop Organized by the Department:

Sr. No.	Name of activity	Name of organizing staff	Date	Participants
1	Three days hands – on Workshop on Arduino using MATLAB	Prof. P. R. Wankhede	28th -30th Dec. 2015	Students of third & final year & faculty: 20
2	One day workshop on “Scheme updation of the programmes in board of studies (Electronics Engg.),SGB Amravati University”	Prof. G. S. Gawande Prof. D. D. Nawgaje	30th Dec. 2015	All Engineering Colleges in SGBAU Faculty: 70

Publisher:

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Head ,

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