



**Shri Sant Gajanan Maharaj College of  
Engineering**  
**SHEGAON, MS, INDIA**  
**Newsletter June 2016**

**DEPARTMENT OF ELECTRONICS AND  
TELECOMMUNICATION ENGINEERING**  
**JUNE 2016**  
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**World Class  
Engineering and  
Management in  
The  
Environment of  
Spiritual  
Foundation to  
Serve the Global  
Society**

### **ABOUT DEPARTMENT:**

The department has taken development as a continuous process and assured this through its mission. The department has blend of senior, young and dynamic staff. The department has following laboratories equipped with modern instruments and advanced software.

- Digital Signal Processing Lab. / VLSI-Embedded System Design. Lab.
- Power Electronics (Instrumentation) Lab. / Project Lab.
- M.E. Computer Lab
- Communication Engineering (UHF) Lab.
- Analog and Digital Circuit Lab./ Electronics Devices and Circuit Lab.
- Electronics Work Shop Lab

To cope up with the advanced curriculum of the university, all these laboratories are equipped with special instruments like Wobuloscope, Emulator, OTDR simulation package, Xilinx 6.4, PLDs, TMS 380C50 DSP trainer kits, Klystron & Gunn benefits.

Dr. G.S. Gawande Head, Department of Electronics & Tele. Engineering

### **VISION:**

- To impart quality education and excel in Electronics and Telecommunication Engineering research to serve the global society.

### **MISION:**

- To develop excellent learning center through continuous interaction with Industries, R&D centers and Academia.
- To produce competent, entrepreneurial and committed Electronics and Telecommunication Engineers.
- To develop state-of-the -art infrastructure, centers of excellence and to pursue research of global and local relevance.
- To inculcate ethical, spiritual and human values to serve the global society.

## **Program Educational Objectives (PEOs)**

- **PEO1:** To produce Electronics & Telecommunication engineers with a strong foundation of Mathematics, Science and Technology to fulfill needs of society.
- **PEO2:** To enable students to innovate design, simulate, develop, analyze and test hardware and software components for offering solutions to real life situations using state-of-the-art infrastructure and R&D facilities.
- **PEO3:** To nurture students with professional attitude, leadership, entrepreneurship, effective communication, teamwork & multi-disciplinary approach to serve in national and multinational organizations.
- **PEO4:** To inculcate ethical, moral and environment friendly values in students.

## **Program Outcomes (POs)**

**At the end of the program, a graduate will –**

- [1] Develop an ability to apply knowledge of mathematics, science and engineering in appropriate fields of Electronics & Tele-communication engineering practice. (Engg. Knowledge)
- [2] Inculcate skills to design and conduct experiments, as well as to analyze and interpret data. (Problem Analysis)
- [3] Develop an ability to disseminate the knowledge of Components & Circuit Design, Communication Systems, Micro-computing & Digital System Design & implement them with latest technology. (Design ability)
- [4] Develop an ability to use research based knowledge using relevant literature survey to design experiments, mini projects, projects to analyze, interpret and synthesis the results to get valid conclusions. (Solutions to complex problem)
- [5] Develop an ability to use modern engineering & IT tools and techniques to solve industrial and engineering problems. (Use of modern tools)
- [6] Show the understanding of impact of engineering solutions on the society and also will be aware of contemporary issues. (The Engineer and the society)
- [7] Develop an understating of professional, moral, cultural and environmental responsibilities while imparting Electronics & Telecommunication Engineering solutions. (Environment and sustainability)
- [8] Inculcate an ability to apply principles of professional ethics and responsibilities while implementing engineering practices. (Professional responsibilities & Ethics)
- [9] Demonstrate an ability to work efficiently in the capacity of an individual, member or leader of a diverse team including multi-disciplinary tasks. (Team work)
- [10] Communicate effectively in both verbal and written form not only with engineers but also with the society at large. (Communication skills)
- [11] Be able to apply principles of project management and finance while effectively completing engineering and multi-disciplinary projects as an individual, a member or a leader. (Project management and finance)
- [12] Constantly perceive the need of consistent professional up gradation and will pursue independently the technological developments through trainings and higher studies. (Lifelong learning)

## Program Specific Outcomes (PSOs)

- **PSO1** Students will be able to apply the fundamental and design knowledge in the areas of analog and digital circuits and systems for solving the real world engineering problems
- **PSO2** Students will be able to apply the fundamental knowledge for the analysis and development of communication based circuits and systems.

### Recent publication by faculty:

#### Recent journal publication:

Sr. No.	Author	Title of article/ title of book/ Book	Journals details	Year of publication
1.	Dr G S Gawande & Dr K B Khanchandani	Design, implementation and analysis of power efficient polyphase multirate Filters	Third IEEE International Conference on Signal Processing and Integrated Networks (SPIN-2016)	Feb. 2016

#### Recent conference publication:

Sr. No.	Author	Title of article/ Title of book/ Book	Conference details	Year of publication
1	Prof. M.N.Tibdewal	Wavelet transform based multiple features extraction for detection of epileptic/non-epileptic multichannel	3 <sup>rd</sup> 2016 International Conference as India Com-2016, New Delhi	March 2016
2		Power line and ocular artifact de-noising from EEG through notch filter and wavelet transform	3 <sup>rd</sup> International conference as India Com-2016, New Delhi	March 2016
3		Multichannel detection of epilepsy using SVM classifier on EEG signal	International conference on computing communication control and automation (ICCUBEA) PCCE, Pune	April 2016

### Faculty achievements:

- Prof. S. B. Patil has completed his Ph.D. work under Dr. R. D. Kanphade as in house research scholar & he has awarded Ph.D. Degree by SGB Amravati University on 20<sup>th</sup> Feb. 2016.
- Prof. D. D. Nawgaje has completed his Ph.D. work under Dr. R. D. Kanphade as in house research scholar & he has awarded Ph.D. Degree by SGB Amravati University on 20<sup>th</sup> Feb. 2016.
- Prof. G. S. Gawande has completed his Ph.D. work under Dr. K. B. Khanchandani as in house research scholar & he has awarded Ph.D. Degree by SGB Amravati University on 20<sup>th</sup> Feb. 2016.
- Mr. A. O. Amalkar (faculty member at SSGMCE for 16 years) has completed his Ph.D. work under Dr. K. B. Khanchandani as research scholar & he has awarded Ph.D. Degree by SGB Amravati University on 22<sup>nd</sup> Feb. 2016.
- Presently 06 faculty members doing their Ph.D. work in research lab of Electronics & Telecommunication Engineering department.

### Guest lecturers arranged in the department in 2015-16:

Sr.No.	Name of activity	Name of organizing staff	Date	Participants
1	Guest lecture by Mr. Vishesh D. Aggrawal, M.Tech. (IIT,Kharagpur) Co-founder and Managing Director of V-chip design and training Pvt. Ltd., Amravati	Prof. D. P. Tulaskar	31 <sup>st</sup> March 2016	BE second year, students all branch

### Student's achievements in 2015-16:

- Final year 4U1 students Mr. Shubham Manekar, Mr. Rohan Sherekar, Mr. Umesh Charpe and Mr. Pranav Pardhi under the guidance of Prof. V. K. Bhangdiya have Won 1<sup>st</sup> prize in paper presentation competition and 2<sup>nd</sup> prize in project competition conducted at JDIET, Yavatmal under "Techno Extreme-16" on 29<sup>th</sup> March 2016. Their paper titled "Automatic street Light control using PLC microcontroller and LM3464" was selected for publishing in international journal of research in science and technology. Their Project title was Efficient Street Lighting.
- Final year 4U1 students Mr. Ankit M. Sorty, Mr. Amit Deokar and Mr. Shrikant Darade under the guidance of Prof. D. P. Tulaskar have Presented Paper in 4<sup>th</sup> International conference on Emerging trends & research in engineering technology & science held at Dr. Rajendra Gode Institute of Technology & Research, Amravati, M.S. India on 30<sup>th</sup> & 31<sup>st</sup> March 2016 & published the paper in International Journal of Pure & Applied Research in Engineering & Technology in April 2016 issue having impact factor 4.226 (ISSN:2319-507X) Paper title was "Design & Analysis of Two Element Microstrip Patch Antennae Array for GPS and Wi-Fi".

**Workshop organized by the department:**

Sr. No.	Name of activity	Name of organizing staff	Date
1.	ARMREB-DRDO sponsored one week STTP on "Microwave passive circuits and antennas design, simulation and measurements"	Prof. V. V. Ratnaparkhi	04 <sup>th</sup> to 09 <sup>th</sup> , January 2016
2.	Technical session on RFIC design issues & application in ARMREB-DRDO sponsored one week STTP on "Microwave passive circuits and antennas design, simulation and measurements"	Dr. K. B. Khanchandani	07 <sup>th</sup> January 2016
3.	Three days workshop on "RF components and antennas design, simulation and measurements"	Prof. V. V. Ratnaparkhi	11 <sup>th</sup> to 13 <sup>th</sup> March 2016

**Industry or Institute visit by students:**

Sr.No.	Industry	Date and total number of students	Name of the faculty involved
1	Industrial tour of 3U1 students, Renu Electronics, Pune	03 to 06 Feb. 2016 (Total 60 Students)	Prof. P. R. Wankhede Prof. P. D. Kale
2	Industrial tour of 3U2 students, Electronica Pvt. Ltd, Pune	08 to 11 Feb. 2016 (Total 47 Students)	Prof. A. N. Dolas
3	Industrial visit Bits-Apogee at BITS Pilani	23 Feb. to 01 March 2016 (Total 04 Students)	Prof. P. R. Wankhede

**Student Internship and summer training:**

Sr.No.	Name of the student	Year	Name of the company	Duration/Date
1	Rahul Agrawal	3 <sup>rd</sup> year	Siemens Pvt. Ltd. Nashik	1 Month
2	Yashwant Tayde	year		
3	Pallavi Bhandarkar	ME 2 <sup>nd</sup> year	CSIR-CEERI Pillani	1 Year

**Final year placement statistics :**

<b>Sr. No.</b>	<b>Name of student placed</b>	<b>Name of the employer</b>	<b>Date</b>
1.	Ankita Dhage	TCS Ltd., Pune	23/03/16
2.	Madhuri Rohankar	TCS Ltd., Pune	23/03/16
3.	Neha Bonde	TCS Ltd., Pune	23/03/16
4.	Pradnya Dahiwalale	TCS Ltd., Pune	23/03/16
5.	Pradnya Shewale	TCS Ltd., Pune	23/03/16
6.	Prapti Ghayar	TCS Ltd., Pune	23/03/16
7.	Prerna Joshi	TCS Ltd., Pune	23/03/16
8.	Shraddha Vasu	TCS Ltd., Pune	23/03/16
9.	Amit Deokar	TCS Ltd., Pune	23/03/16
10.	Ankit Bagdiya	TCS Ltd., Pune	23/03/16
11.	Mayur Vyawahare	TCS Ltd., Pune	23/03/16
12.	Shrikant Darade	TCS Ltd., Pune	23/03/16
13.	Sunayana Jadhav	TCS Ltd., Thane	23/03/16
14.	Aabha Raut	Polaris, Pune	26/01/16
15.	Ankit Sorty	Polaris, Pune	26/01/16
16.	Ankita Mishra	Polaris, Pune	26/01/16
17.	Ashutosh Chopde	Polaris, Pune	26/01/16
18.	Lubdha Thakre	Polaris, Pune	26/01/16
19.	Pallavi Kapate	Polaris, Pune	26/01/16
20.	Pooja Bagade	Polaris, Pune	26/01/16
21.	Pooja Varma	Polaris, Pune	26/01/16
22.	Rahul Agrawal	Polaris, Pune	26/01/16
23.	Samiksha Halge	Polaris, Pune	26/01/16
24.	Shweta Barabde	Polaris, Pune	26/01/16
25.	Shwetali Deshmukh	Polaris, Pune	26/01/16

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