



**Shri Sant Gajanan Maharaj College of
Engineering**
SHEGAON, MS, INDIA
Newsletter December 2017

**DEPARTMENT OF ELECTRONICS AND
TELECOMMUNICATION ENGINEERING
DECEMBER 2017
EDITION #3, VOLUME #1**

**World Class
Engineering and
Management in
The Environment
of Spiritual
Foundation to
Serve the Global
Society**



ABOUT:

The Department of Electronics and Telecommunication Engineering is one of the major department of SSGMCE, Shegaon established in 1983 offering programs: Under Graduate, Post Graduate and Ph.D. It is affiliated to Sant Gadge Baba Amravati University, Amravati, recognized by AICTE, New Delhi and approved by DTE, Maharashtra. The Undergraduate program of the Department of Electronics and Telecommunication Engineering has the recognition of being accredited four times by NBA, AICTE, New Delhi. The post graduate program was also accredited once by NBA, AICTE, New Delhi.

All the laboratories in the department are well equipped to run the program specific curriculum prescribed by the University. All laboratories are recognized and approved as research laboratories for Ph.D. work by SGB Amravati University.

Dr. G.S. Gawande
Head, Department of Electronics & Tele. Engineering

The department has following laboratories:

- Digital Signal Processing Lab.
- VLSI-Embedded System Design. Lab.
- Power Electronics Lab/ Instrumentation Lab.
- Project Lab.
- Communication Engineering Lab.
- Analog and Digital Circuit Lab.
- Electronics Devices and Circuit Lab.
- Electronics Work Shop Lab.
- Cadence Laboratory.
- PG Laboratory I
- PG Laboratory II

VISION:

- To impart quality education and excel in Electronics and Telecommunication Engineering research to serve the global society.

MISION:

- To develop excellent learning center through continuous interaction with Industries, R&D centers and Academia.
- To produce competent, entrepreneurial and committed Electronics and Telecommunication Engineers.
- To develop state-of-the-art infrastructure, centers of excellence and to pursue research of global and local relevance.
- To inculcate ethical, spiritual and human values to serve the global society.

Program Educational Objectives (PEOs)

- **PEO1:** To produce Electronics & Telecommunication engineers with a strong foundation of Mathematics, Science and Technology to fulfill needs of society.
- **PEO2:** To enable students to innovate design, simulate, develop, analyze and test hardware and software components for offering solutions to real life situations using state-of-the-art infrastructure and R&D facilities.
- **PEO3:** To nurture students with professional attitude, leadership, entrepreneurship, effective communication, teamwork & multi-disciplinary approach to serve in national and multinational organizations.
- **PEO4:** To inculcate ethical, moral and environment friendly values in students.

Program outcomes (POs)

At the end of the program, a graduate will –

- [1] Develop an ability to apply knowledge of mathematics, science and engineering in appropriate fields of Electronics & Tele-communication engineering practice. (Engg. Knowledge)
- [2] Inculcate skills to design and conduct experiments, as well as to analyze and interpret data. (Problem Analysis)
- [3] Develop an ability to disseminate the knowledge of components & circuit design, communication systems, micro-computing & digital system design & implement them with latest technology. (Design ability)
- [4] Develop an ability to use research based knowledge using relevant literature survey to design experiments, mini projects, projects to analyze, interpret and synthesis the results to get valid conclusions. (Solutions to complex problem)
- [5] Develop an ability to use modern engineering & IT tools and techniques to solve industrial and

- engineering problems. (Use of modern tools)
- [6] Show the understanding of impact of engineering solutions on the society and also will be aware of contemporary issues. (The engineer and the society)
 - [7] Develop an understating of professional, moral, cultural and environmental responsibilities while imparting Electronics & Telecommunication engineering solutions. (Environment and sustainability)
 - [8] Inculcate an ability to apply principles of professional ethics and responsibilities while implementing engineering practices. (Professional responsibilities & ethics)
 - [9] Demonstrate an ability to work efficiently in the capacity of an individual, member or leader of a diverse team including multi-disciplinary tasks. (Team work)
 - [10] Communicate effectively in both verbal and written form not only with engineers but also with the society at large. (Communication skills)
 - [11] Be able to apply principles of project management and finance while effectively completing engineering and multi-disciplinary projects as an individual, a member or a leader. (Project management and finance)
 - [12] Constantly perceive the need of consistent professional up gradation and will pursue independently the technological developments through trainings and higher studies. (Life long learning)

Program Specific Outcomes (PSOs)

- **PSO1** Students will be able to apply the fundamental and design knowledge in the areas of analog and digital circuits and systems for solving the real world engineering problems
- **PSO2** Students will be able to apply the fundamental knowledge for the analysis and development of communication based circuits and systems.

Recent publication by faculty:

Recent journal publication:

Sr. No.	Author	Title of article/ Title of book/ Book	Journals details	Volume and issue No.	Year of publication	ISSN/ ISBN No.
1	Prof. V. N Bhonge	Smart irrigation system using Internet of things	International journal of advance research in science and engineering	Volume No. 06	October 2017	2319- 8354
2	Dr.G.S. Gawande Dr. K. B. Khanchandani Dr.S. Balpande Prof. M Bhaiyya, Prof. D. Tulaskar,	Design and development of AI foil Ide electrode Based ambient harvester	Institute for smart structures and systems, Isss, 2017, Iisc, Bangalore	--	Proceeding	--
4	Dr.D.D. Nawgaje	Intelligent washing machine using soft computing	IJARCCCE	Volume 6, Issue 8	August 2017	Issn:2319 -5490
5		Design and implementation of an energy load management using artificial neural network	IJARCCCE	Volume 6, Issue 9	Sept 2017	Issn:2319 -5490
6		implementation of fuzzy based washing machine	IJAREEIE	Volume 6, Issue 9	Sep 2017	Issn: 2278- 8875
7	Prof. P Chavhan, Dr. Santosh B. Patil	Haar wavelet based iris recognition system for secure access using MATLAB	International journal of innovative research in computer and communication engineering	Volume 5, Issue 6	June 2017	--
8	Dr.G.S. Gawande & Dr. K. B. Khanchandani	Identification of plant Species based on leaf Images	International journal of informative & futuristic research	Volume4 Issue 11	July 2017	ISSN: 2347- 1697
9		A novel reduced local search algorithm for optimization of digital filter coefficients	Submitted to algorithmica (springer)	--	--	--

Guest lecturers arranged in the department:

- Guest lecture on "**Recent trends in signal processing and its applications**" was organized by Prof. V. K. Bhangdiya on 28th August 2017 for all the BE final year and PG Students of ExTC Department. The lecture was delivered by Prof. Piyush M. Asolkar, Asst. Professor at Shri Ramdeobaba College of Engineering and Management, Nagpur.
- Guest lecture on "**Industrial revolution and global challenges**" was organized by Prof.V.N.Bhonge and Prof.D.P.Tulaskar on 11th October 2017 for all the students of ExTC department. The lecture was delivered by Dr. A. A. Gathol (Ex. Director of COEP, Ex. vice chancellor of Dr.BATU,Lonere).

List of toppers in 2017:

Year	Name of students	University rank	CGPA
2017	Ku. Aditi Subodh Patvardhan	II	9.07
2017	Ku. Payal Dilip Agrawal	VIII	8.84
2017	Ku. Komal Mahavir Thanvi(M.E.)	I	8.96

Students internship statistics in December 2017

Sr. No.	Name of the Student	Name of the Company	Duration
1.	Savita Nikhade	MGIRI LED light manufacturer, Wardha	20/12/2017-29/12/2017
2.	Vishal Mahhale		
3.	Punam Raghatate		
4.	Durga Salam		
5.	Mayuri Kathode		
6.	Prashant Magar		
7.	Yogesh Ghate		
8.	Nilesh Pidurkar		
9.	Nitin Katole		
10.	Amol Kumbhare		
11.	Shubham thorat		

12.	Ganesh Tonchar		
13.	Pratisha Padishalwar	V-Chip design & training Pvt. Ltd.	DPT
14.	Tushar Zombade		
15.	Ravina Sonar		
16.	Vishal Keshavwar	Siemens Pvt. Ltd. Nasik	01 Week
17.	Vishal Magar		
18.	Mahendra Jadhao	SCIR CEERI Pillani	01 Year
19.	Venkatesh Ramilla	Mitsubishi electric Pvt. Ltd. Pune	01 Week
20.	Gaurav Madke		
21.	Anuja Nikhade	Yogiraj control Pvt. Ltd. Wardha	03 Weeks
22.	Madhvi Dongare		
23.	Minal Lote		
24.	Mitali Deshmukh		
25.	Himani Sharma	Reliance Jio, Indore	02 Weeks
26.	Yogita Dote	DOX PRO Robotics Pvt. Ltd. Amaravati (internship + project)	01 year (Internship + Project)
27.	Ashwini Khanole		
28.	Samiksha Deshmukh		
29.	Priya Bunde		
30.	Ashwini Pawar	V-chip design, Pune	(3 Weeks)
31.	Bhagyashri Pohanpate		
32.	Bhagyashri Wani		
33.	Sandhya Solanke		

Participation in inter-institute events by students:

Sr.No	Name	Date	Event	Conducted by	Event outcome
1.	Sagar Hoge	28-29 Dec 2017	Avishkar, student research competition	Sant Gadge Baba Amaravati Univercity, Amaravati	Participated
2.	Piyush Prakash Shinde				
3.	Laxmikant Vishnu Sapkal				
4.	Shubham Devidas Nasare				
5.	Sharad Mahale				
6.	Shreya Bharapate	26-29 Sept 2017	Youth festival, inter collegiate cultural festival	G.H.Raisoni college of engineering & management, Amaravati	Participated
7.	Anjali Fadnavis				
8.	Abhilash Ravankar				
9.	Prashant Wankhede				
10.	Prajakta Gaikwad				
11.	Gajesh Agrawal				
12.	Nagesh Kumar	21-23 Sept 2017	Inter collegiate yoga championship	Neheru mahavidhyalaya, nerparsopant Dist.Yavatmal	Participated
13.	Ku. Bhavana Chaudhari				
14.	Ku. Ravina Sonar				
15.	Ku. Veena Bhojar				
16.	Ku. Mayuri Changale				

Workshop organized by the department:

- Two days workshop on “**Research methodology**” was organized by Dr. G. S. Gawande and Prof. Ms. B. P. Harne on 22nd to 23rd August 2017 for all the students and faculties of our college.
- Workshop on “**Industrial mutomation with practical session on PLC- SCADA**” was organized by Prof. V.N.Bhonge, Prof. D.P.Tulaskar and Prof.K.T.Kahar during 19th to 28th September 2017 for all 3rd year students of ExTC Department.
- One day workshop on "**Circuit design using multisim**" was organized by Prof. A. N. Dolas on 28th September 2017 for all the 2nd and 3rd year students of ExTC Department.
- Two days workshop on "**Hands on digital system design with XILINX**" was organized by Prof. S. P. Badar during 23rd to 24th September 2017 for all third year and final year students of ExTC Department.

Achievement of ExTC department

Prof M. N. Tibdewal was awarded with Ph.D. degree by I.I.T. , Kharagpur on 30th June 2017. The title of his thesis is "Detection and localization of epilepsy from electroencephalogram as a clinical bio-marker"



Collaboration with industry & institute:

S. N.	Industry	Nature of collaboration	Name of The faculty involved
1.	Mitusubhishi Ind. Pvt. Ltd., MIDC, Bhosari, Pune	Lab. developed at institute	Dr. A.M. Agarkar Dr. G. S. Gawande

Industry visit by faculty:

Sr. No.	Industry	Date	Name of The Faculty Involved
1	Industrial tour, Pune (Renu electronics, baner road ghotawade, Pune)	06/09/2017 to 09/09/2017	Dr. R. S. Dhekekar Prof. B.P. Harné Prof. D. P. Tulaskar Prof. M.L. Bhaiyya Mr. S.P. Satal
2	Industrial visit, auroville (Pondichary)	08/11/2017 to 13/11/2017	Dr. R. S. Dhekekar Prof. A.N. Dolas
3	Industrial visit, Nashik (Simens)	22/11/2017 to 25/11/2017	Dr. G. S. Gawande Dr. R. S. Dhekekar Dr. M.N. Tibdewal

Community development and extension services offered by the department:

- A workshop was organized by Prof. V.N.Bhonge and Prof D.P.Tulaskar for the students of Z.P.High school, Paturda on 8th December 2017. Class 9th and 10th students were participated in the workshop and come to know about the new technologies in electronics world.

Publisher:

Dr.G.S.Gawande

Head ,

Electronics and Telecommunication Engineering

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Chief Editor: Prof. V. N. Bhonge,

Prof. D. P. Tulaskar

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